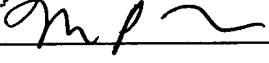


11-23-2002  
Docket No.: GE/99 P 5363  
JC54

2010  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on the date indicated below.

By: 

Date: July 17, 2002

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Anke Krasemann et al.  
Applic. No. : 09/729,058  
Filed : December 4, 2000  
Title : Method of Producing an Oxidation-Protected Electrode for a Capacitive Electrode Structure  
Examiner : Pho M. Luu - Art Unit: 2824

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JUL 26 2002  
TECHNOLOGY CENTER 2800

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97(C)(2)**

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

07/25/2002 57ENDIEI 00000097 09729058  
01 FC:126 150.00 DP

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,939,131 (Kim et al.), dated August 17, 1999;

Japanese Patent Abstract JP 60-173867 (A) (Shinichi), dated September 7, 1985;

Japanese Patent Abstract JP 61-133657 (A) (Hitoshi), dated June 20, 1986;

K.S. Tang et al.: "Trends In DRAM Dielectrics", *Circuits & Devices*, May 1997, pp. 27-34.

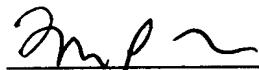
If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicant

s

Date: July 17, 2002

Mark P. Weichselbaum  
Reg. No. 43,248

Lerner And Greenberg, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: (954) 925-1100  
Fax: (954) 925-1101

/bmb